

Fig. 1

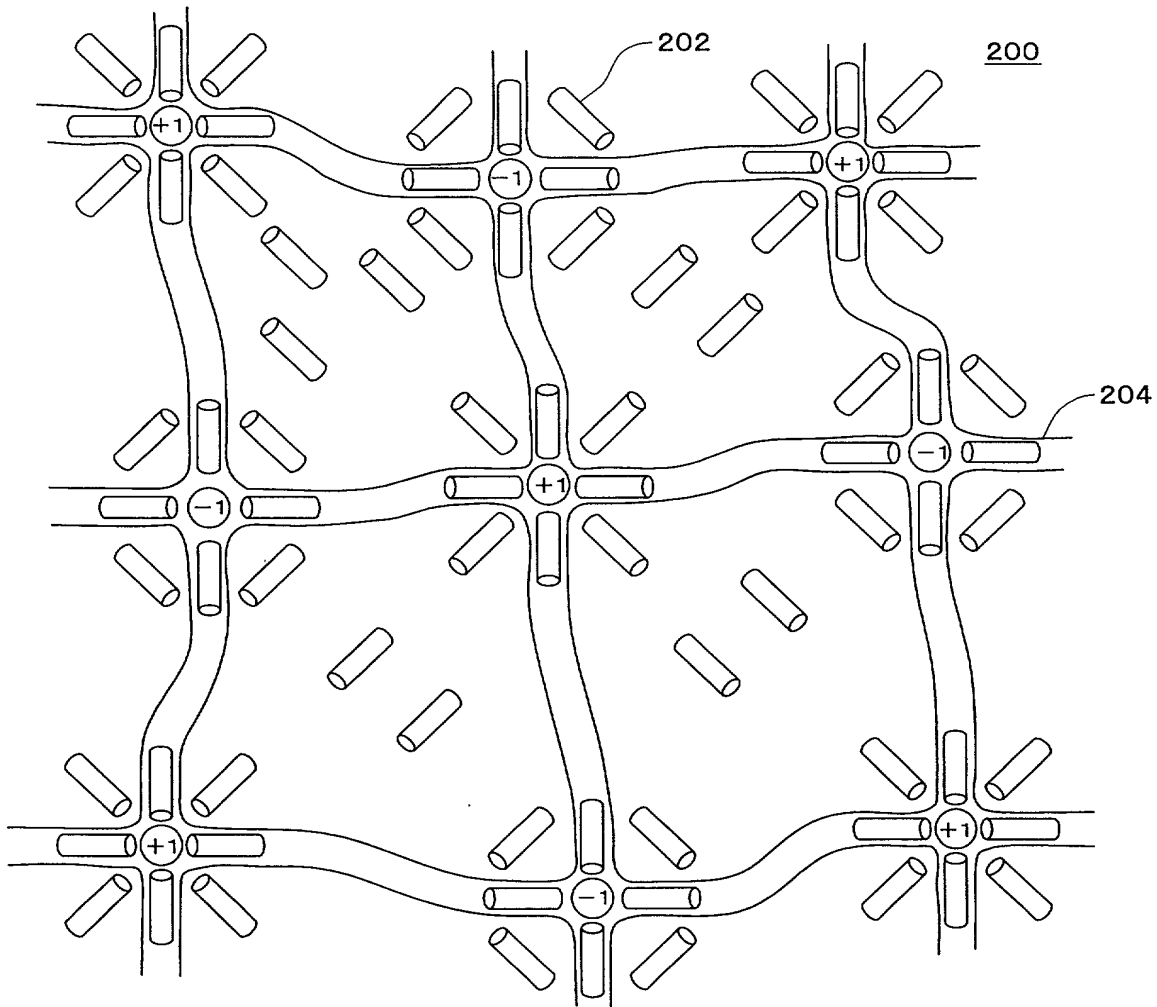
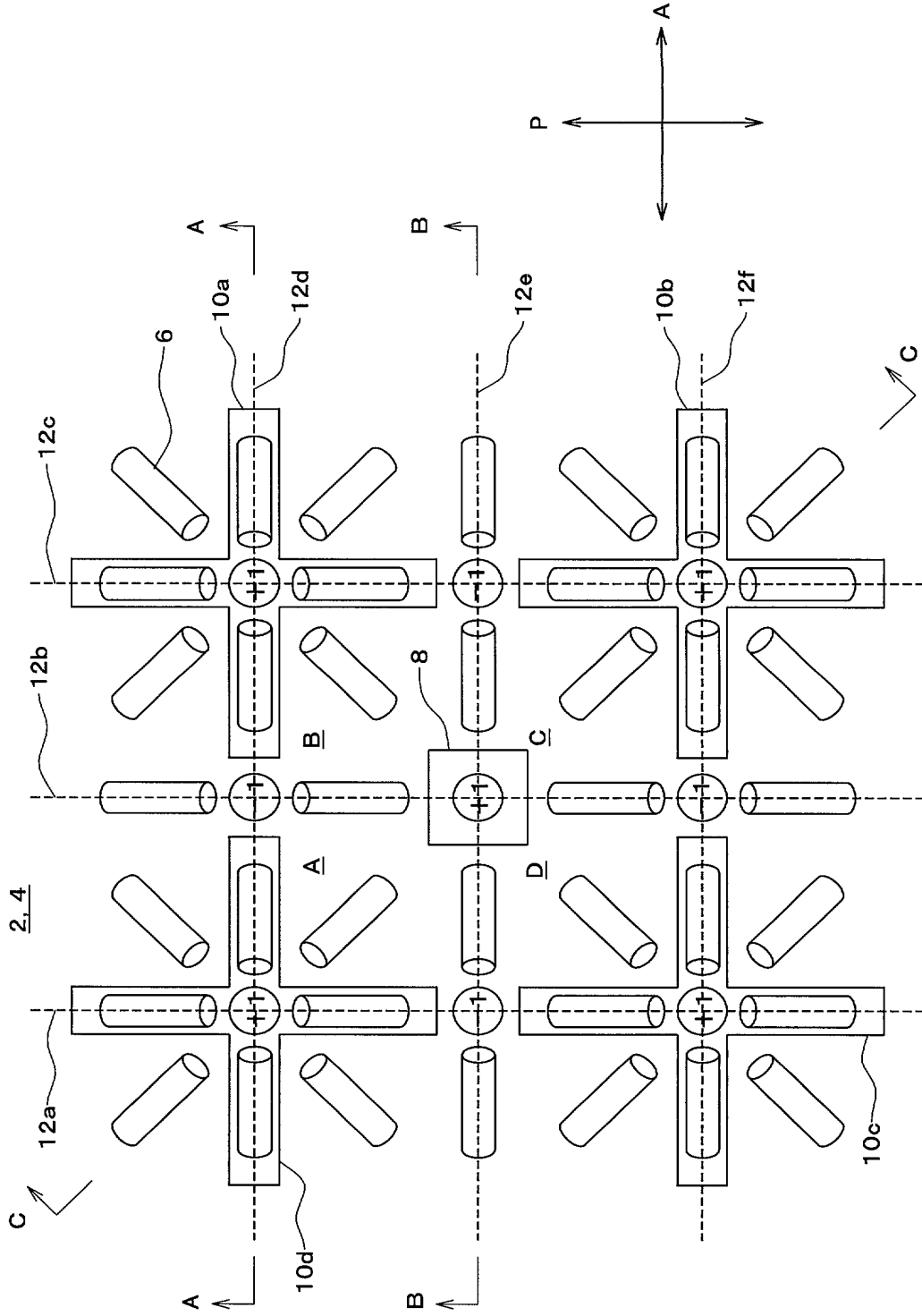
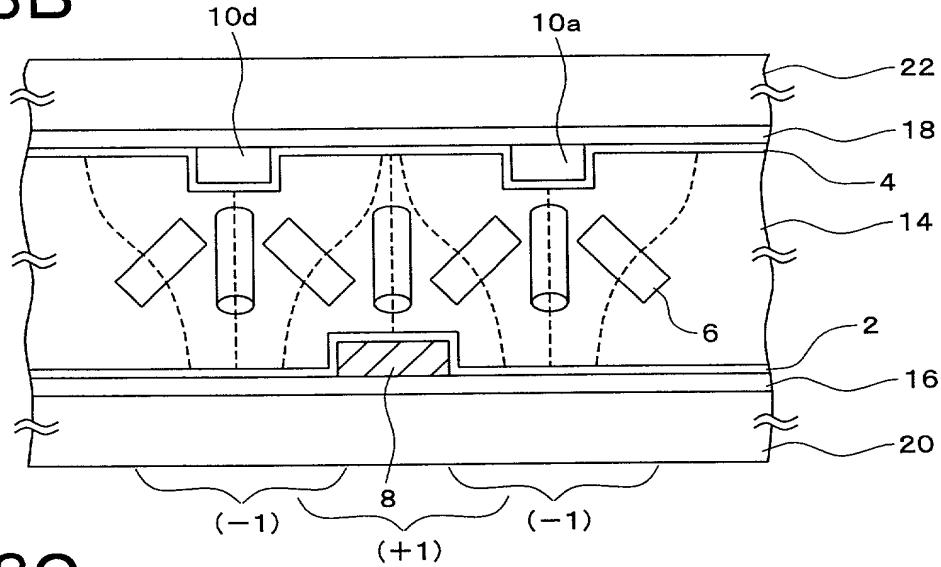


Fig. 2



**Fig. 3B**



**Fig. 3C**

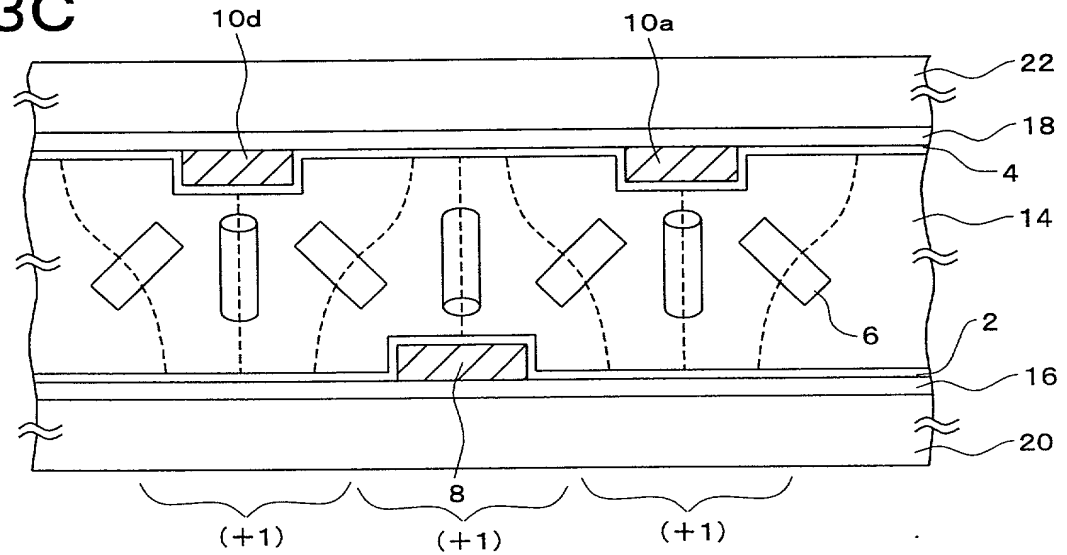


Fig. 4

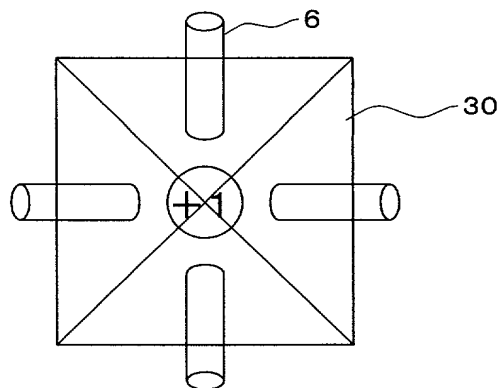


Fig. 5

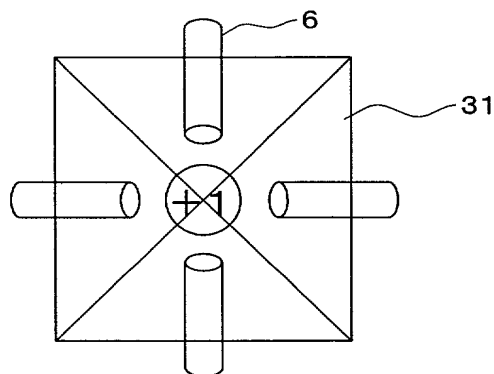


Fig. 6

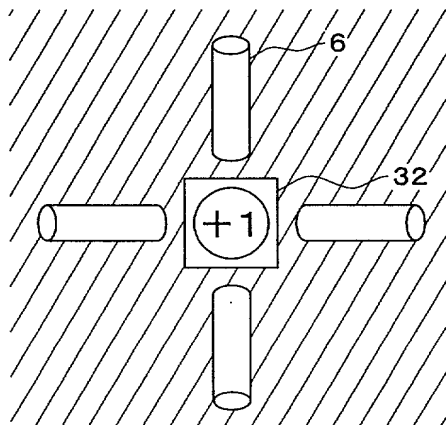


Fig. 7

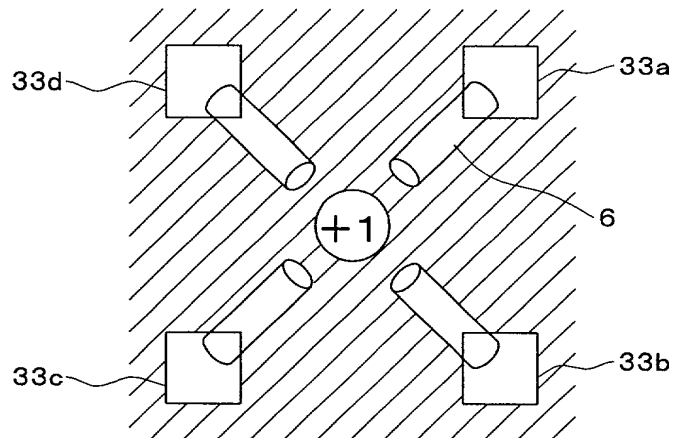


Fig. 8

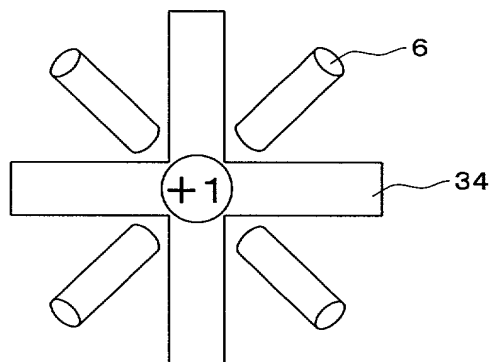


Fig. 9

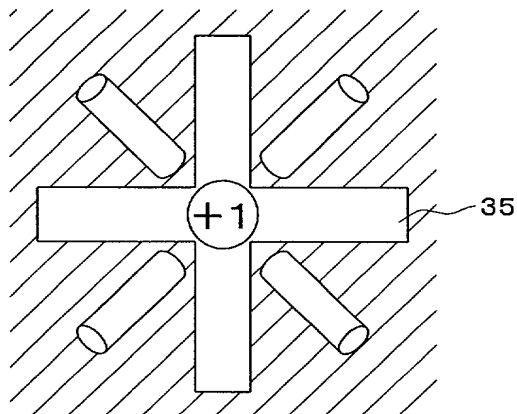


Fig. 10

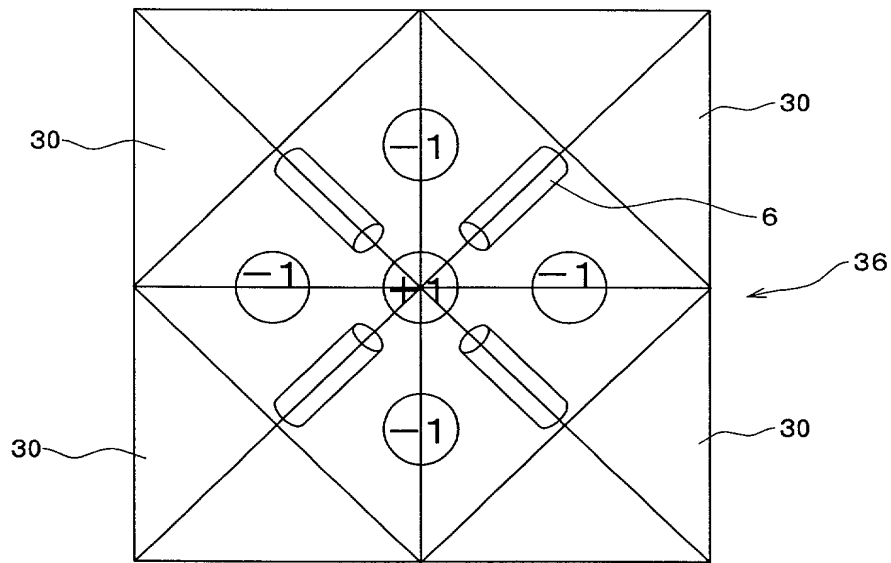


Fig. 11

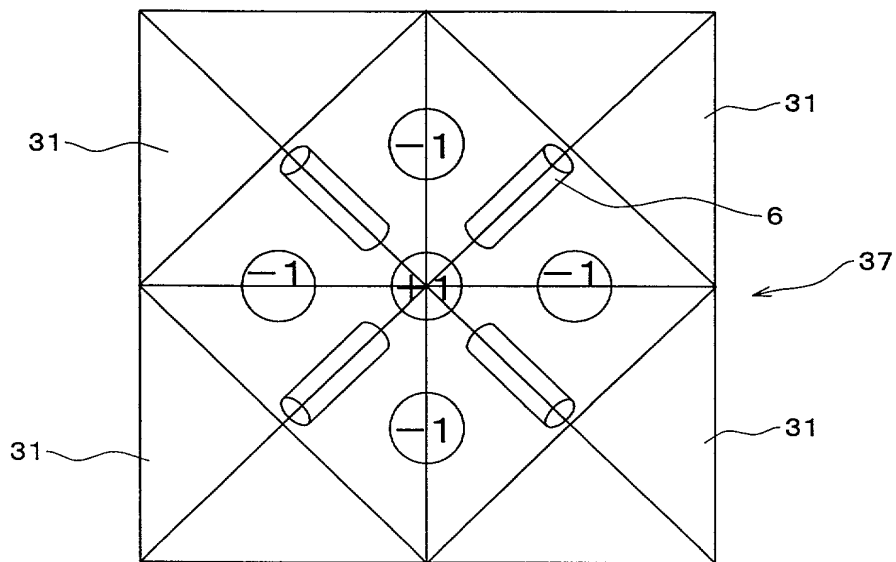


Fig. 12

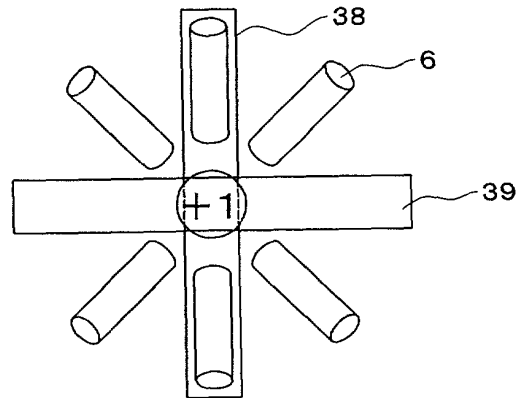


Fig. 13

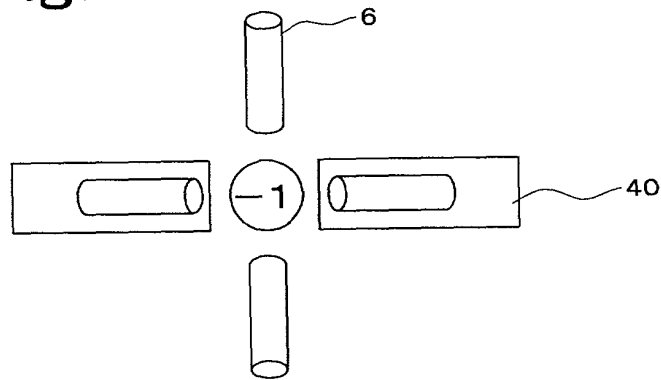


Fig. 14

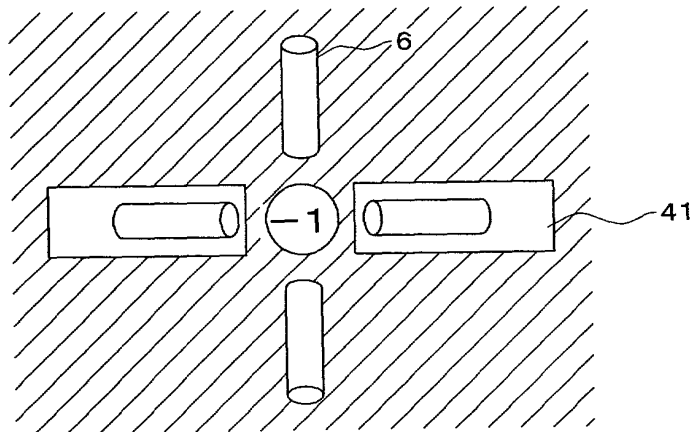


Fig. 15

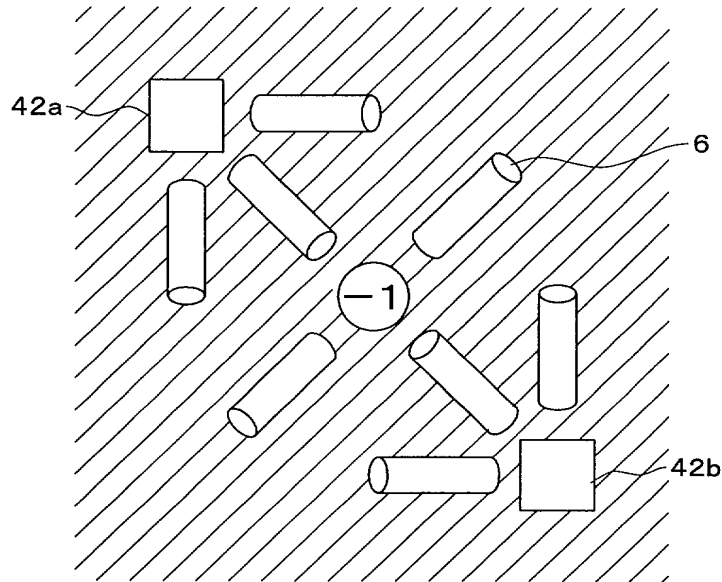


Fig. 16

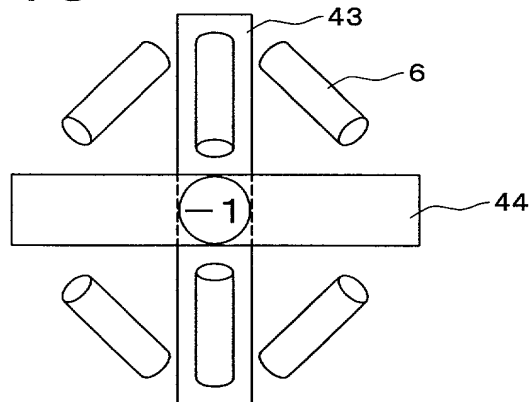


Fig. 17

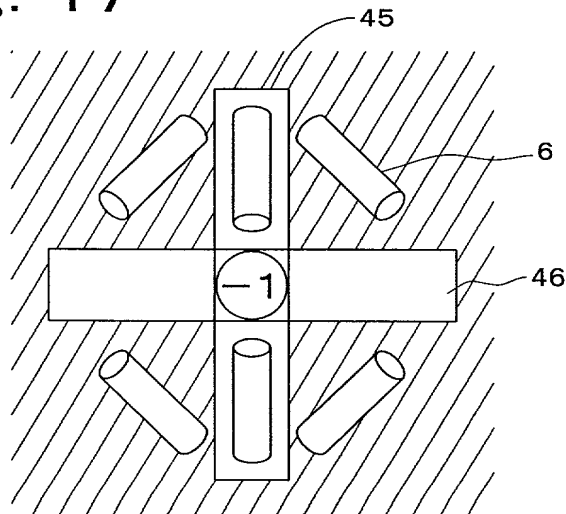




Fig. 18

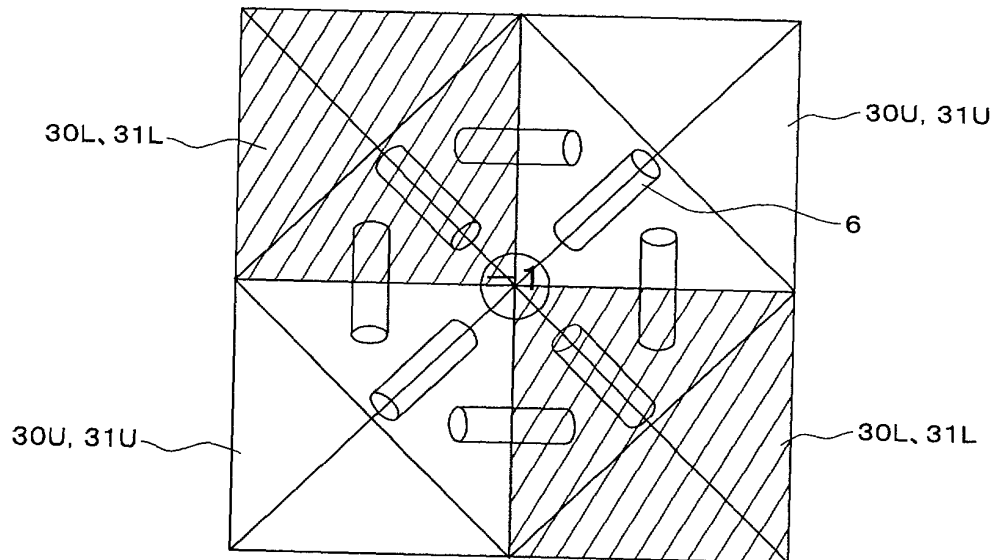


Fig. 19

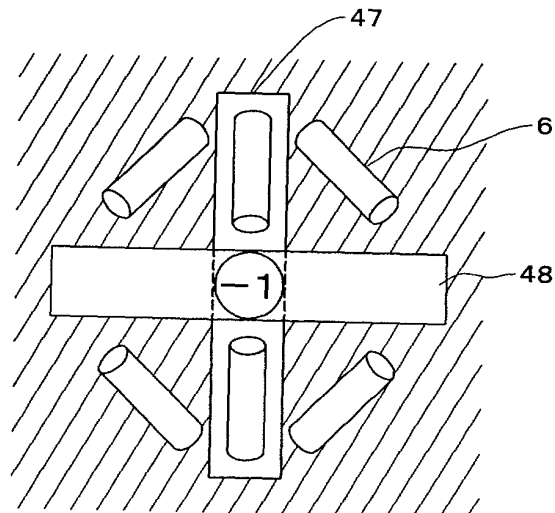


Fig. 20A

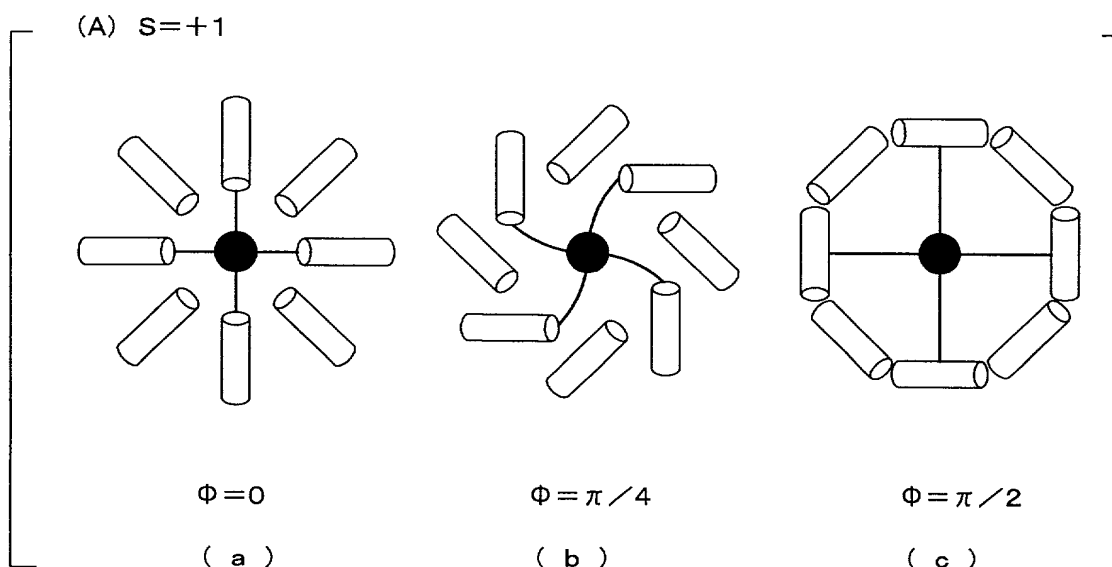


Fig. 20B

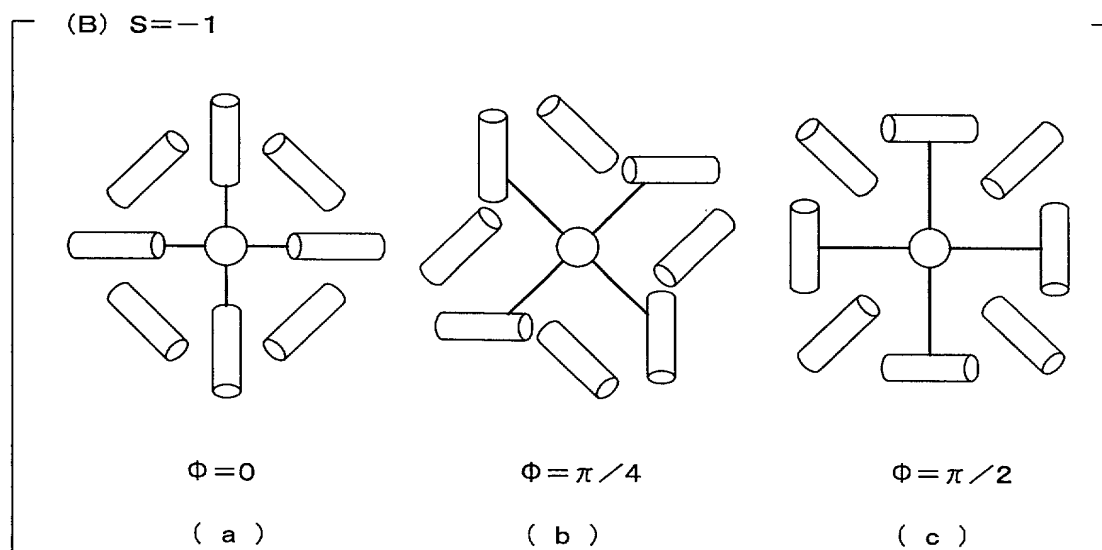


Fig. 21

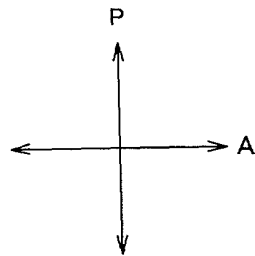
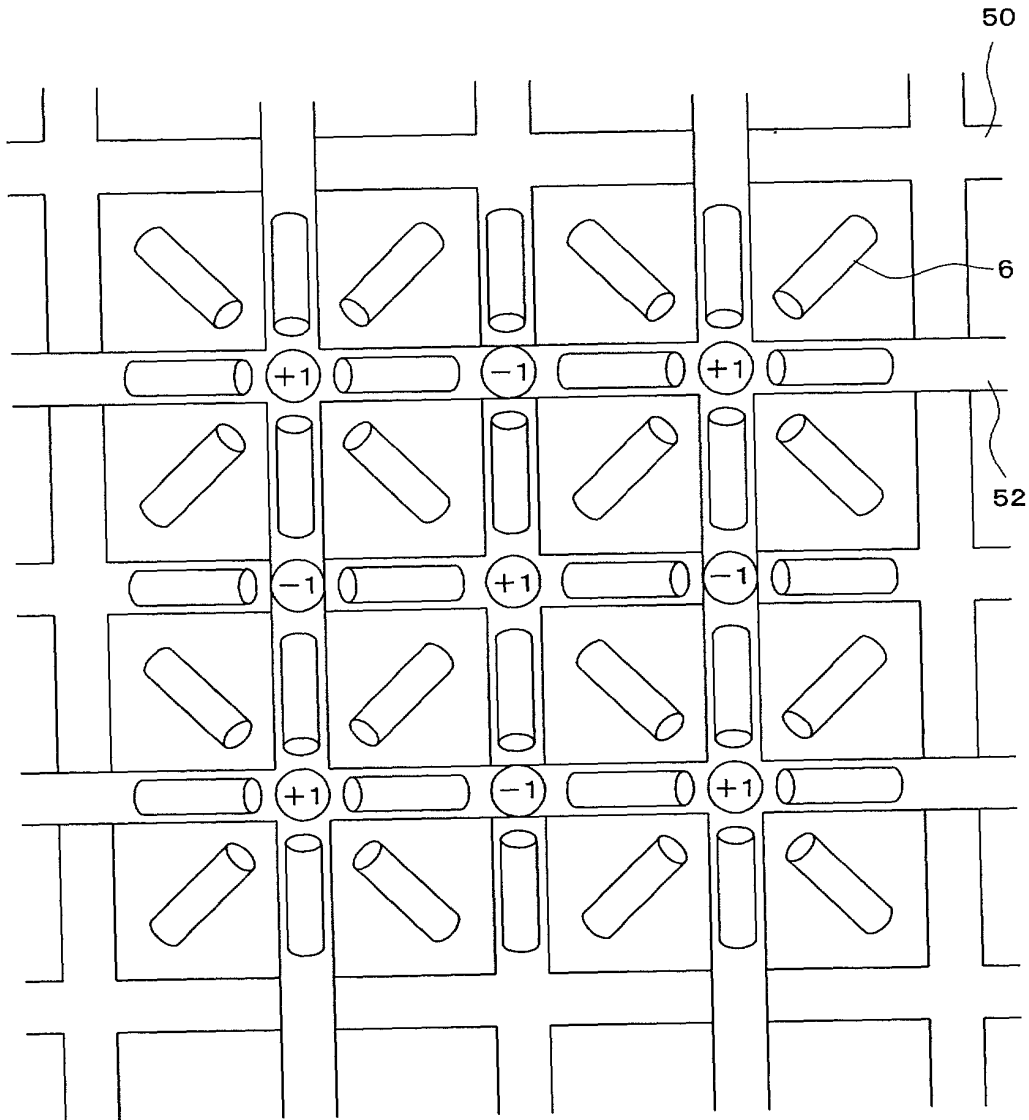


Fig. 22

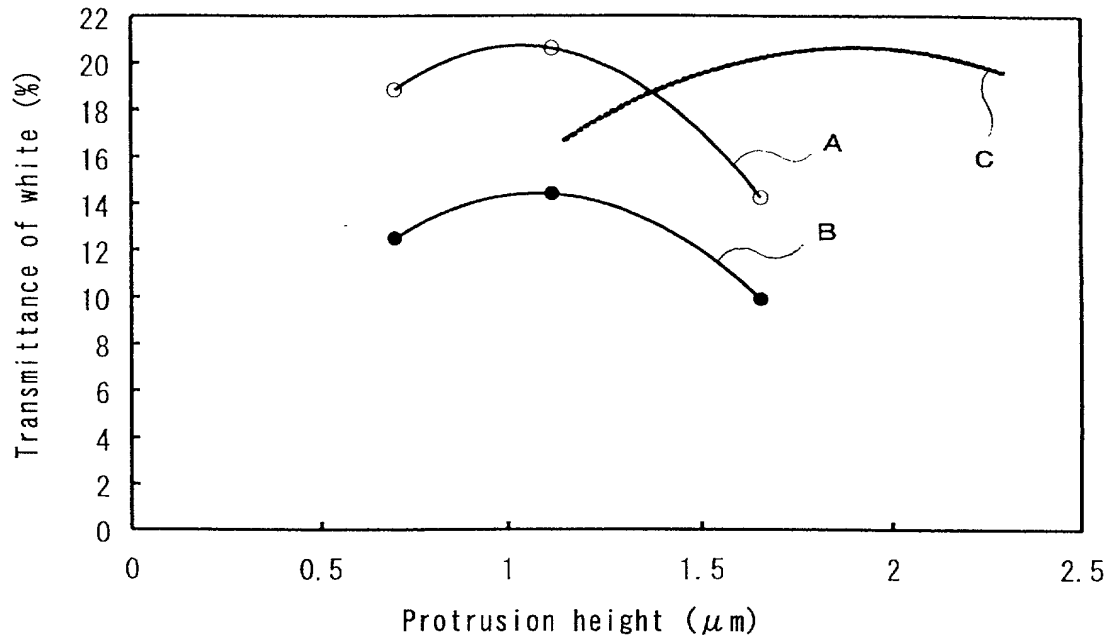


Fig. 23

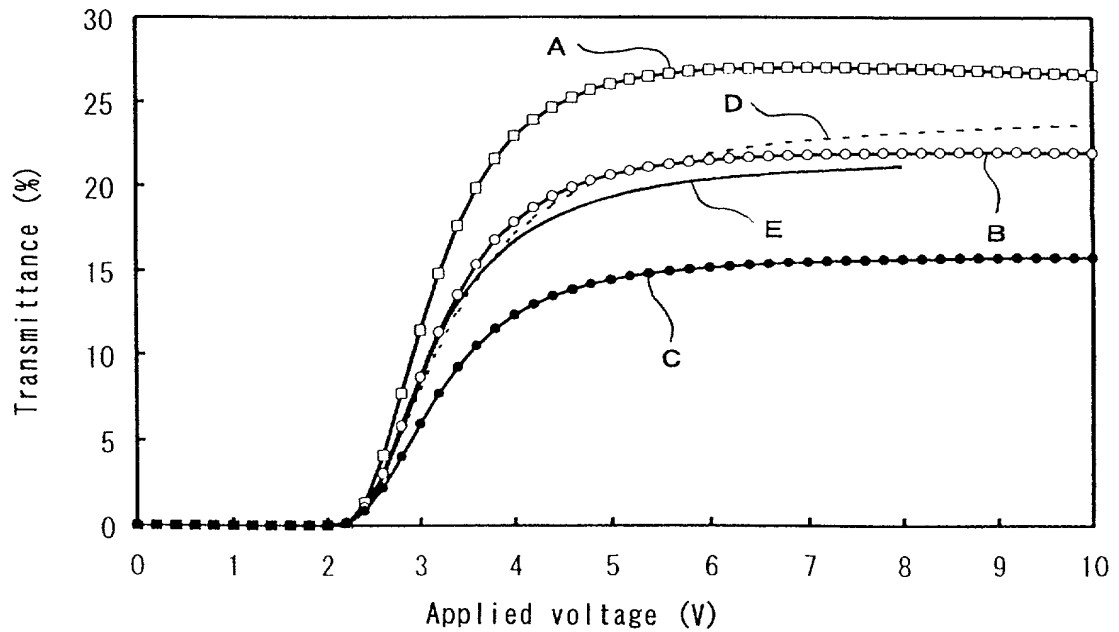


Fig. 24

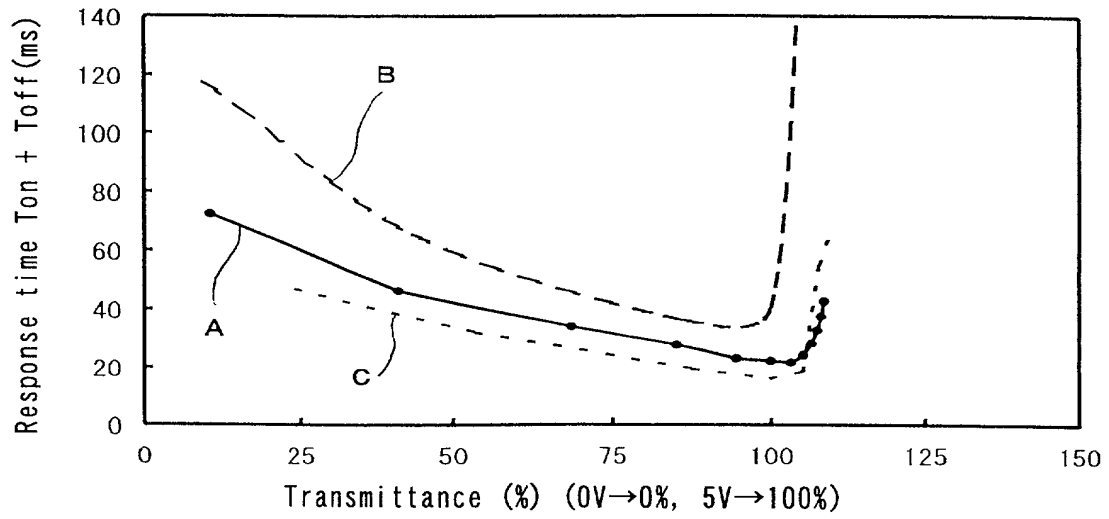


Fig. 25

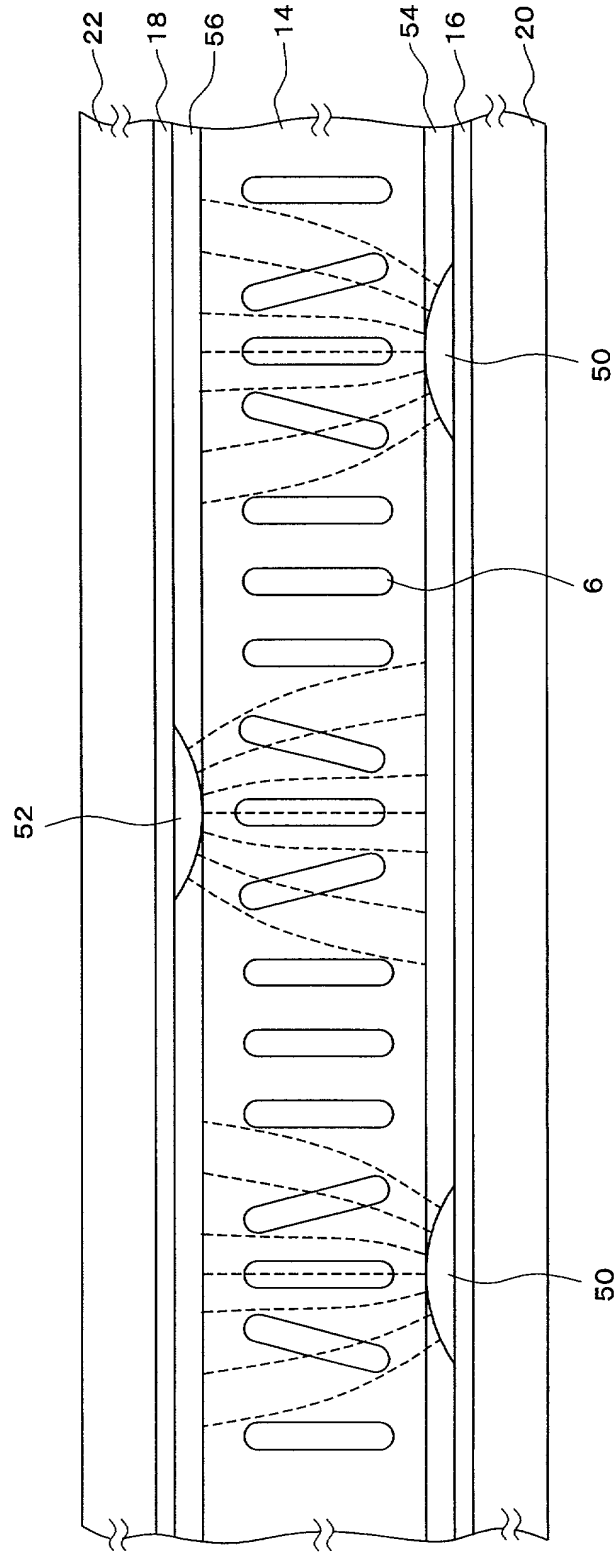


Fig. 26

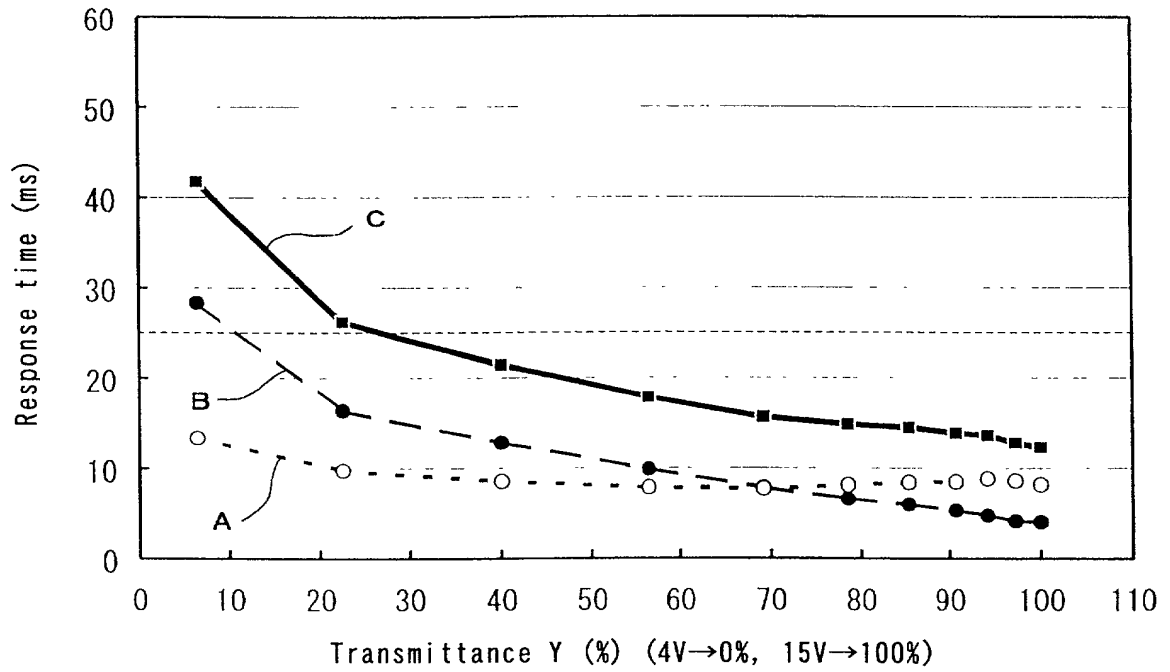


Fig. 27

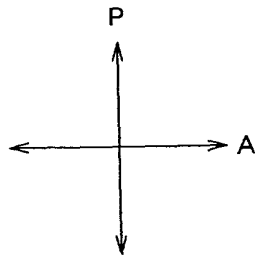
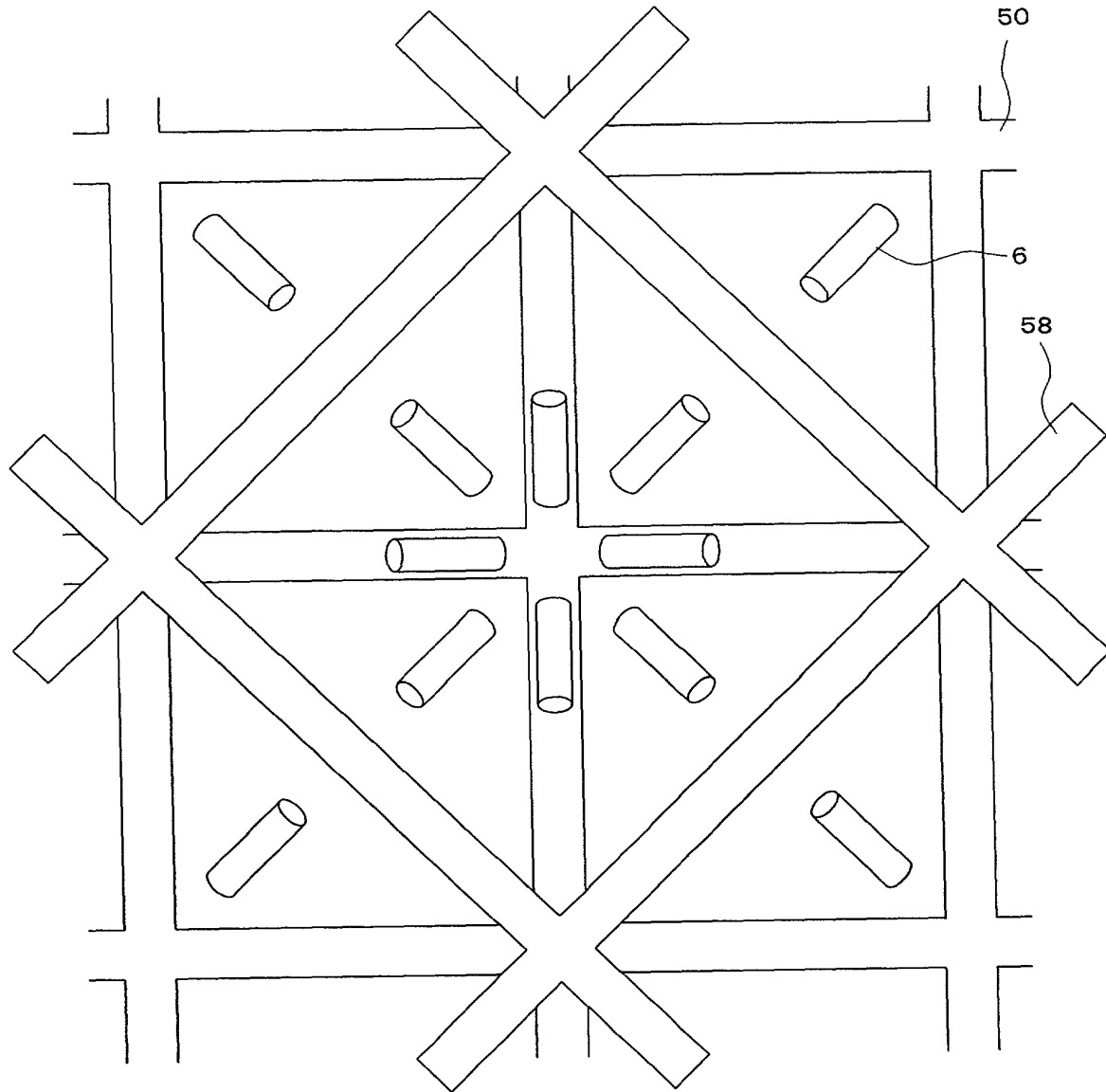




Fig. 28

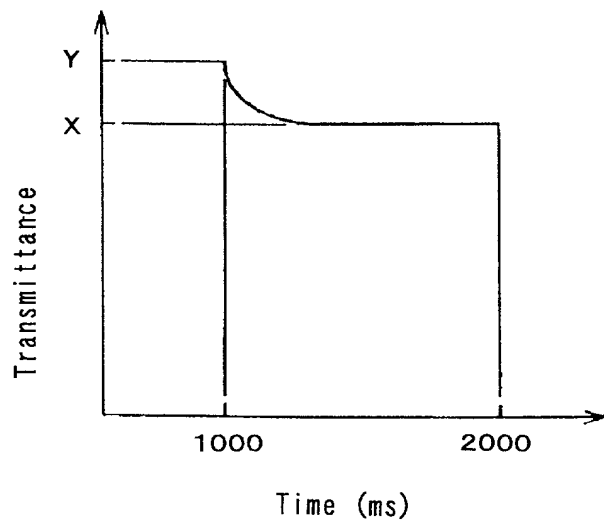


Fig. 29A

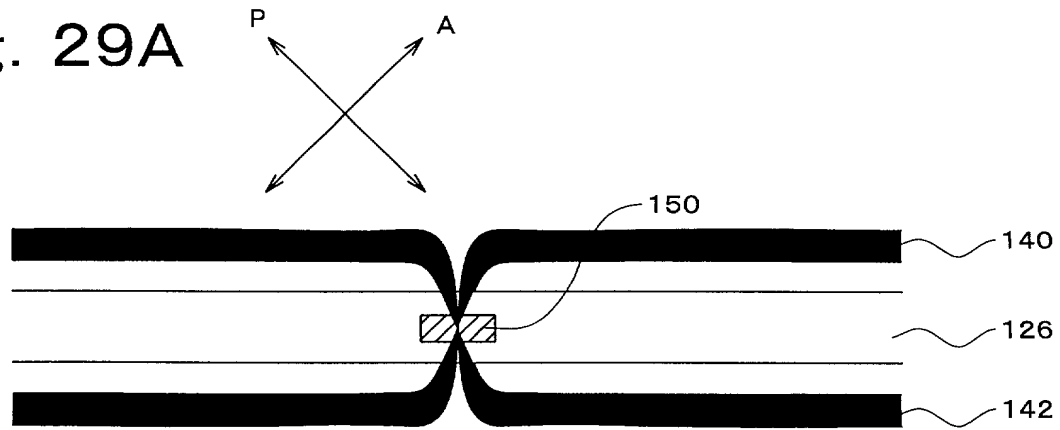


Fig. 29B

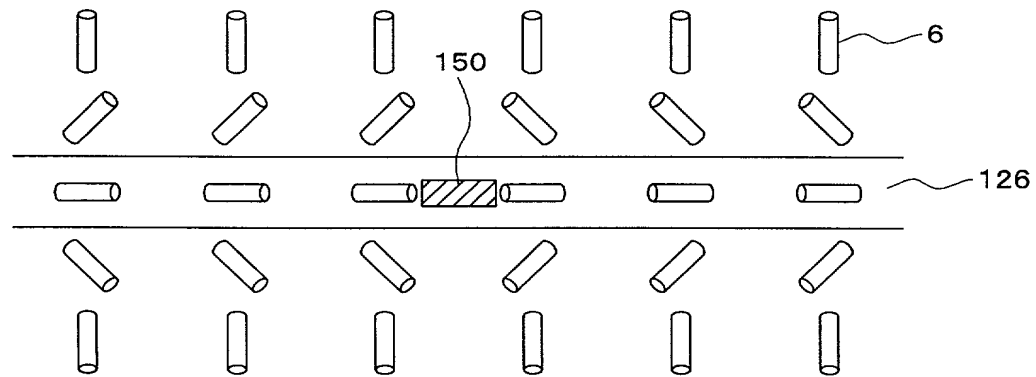
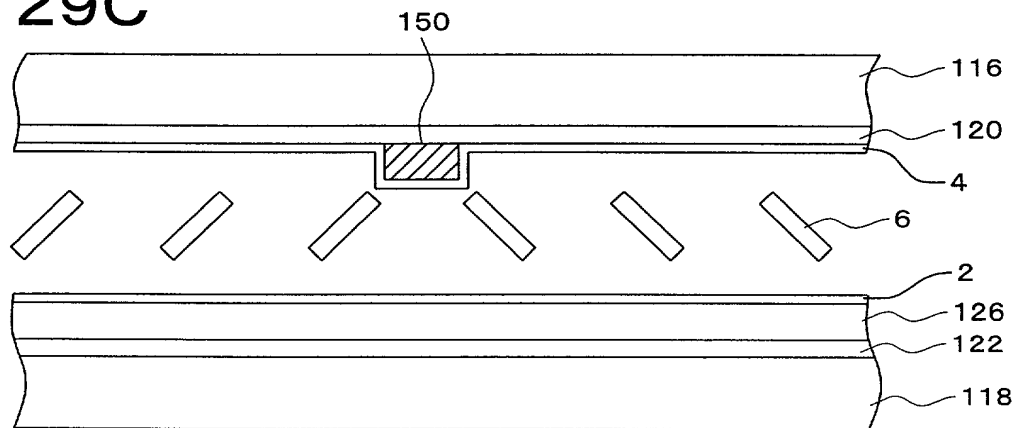


Fig. 29C



The diagram illustrates a complex circuit board layout. It shows a grid of vertical and horizontal lines representing traces or channels. Various components are represented by symbols: circles (some solid black, some white), rectangles, and irregular shapes. Labels point to specific areas or components: 152 at the top corners, 158 near the top center, 154 along the left and bottom edges, 122 in the upper right, 6 in the center, 142 in the middle, 156 on the left, 130 on the lower left, 126 in the lower middle, 140 below 126, 142 below 140, 158 at the bottom center, and 154 at the bottom left. Directional arrows labeled 'P' (pointing up) and 'A' (pointing left) are located in the bottom right corner.

Fig. 31

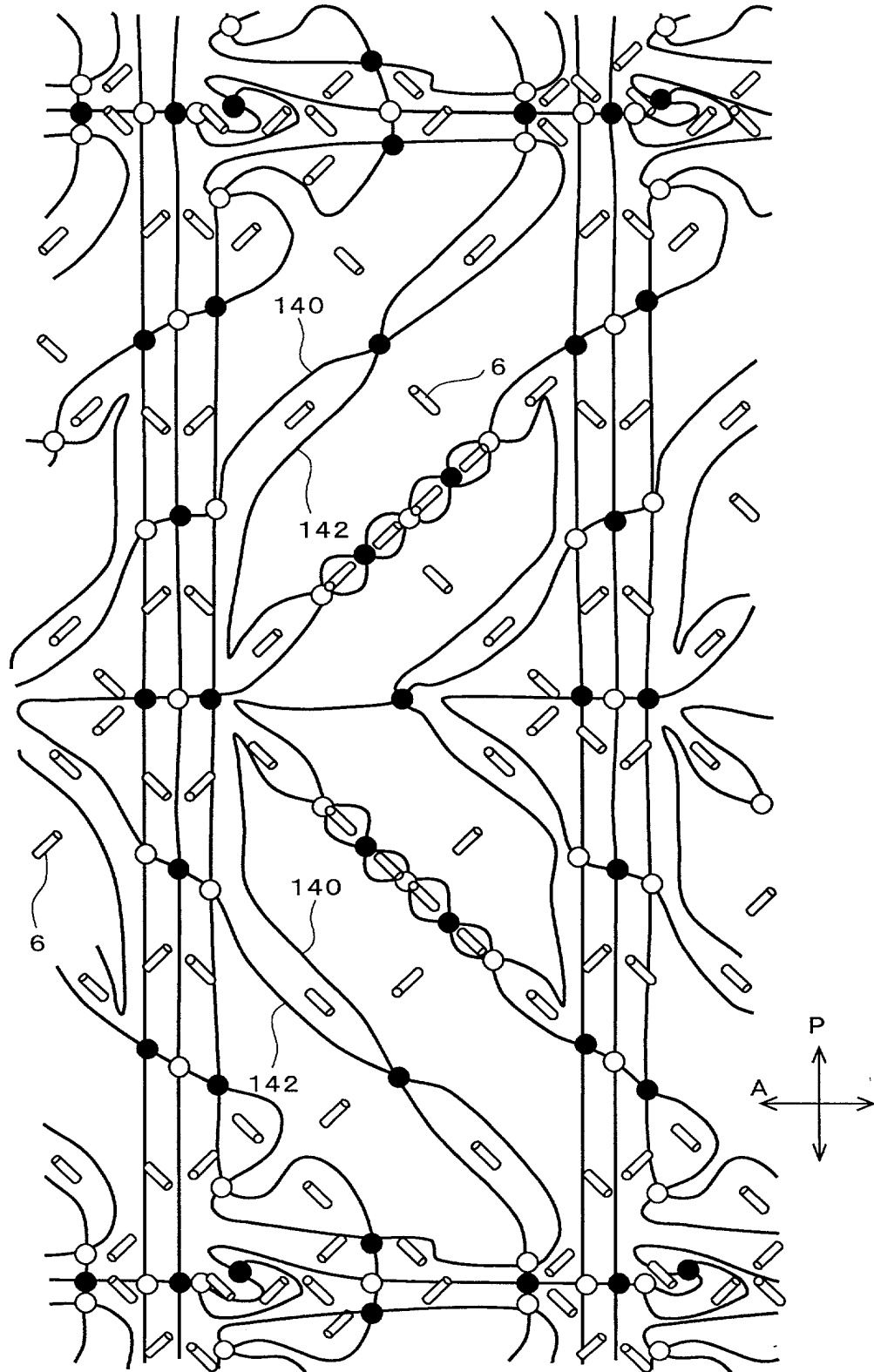


Fig. 32A

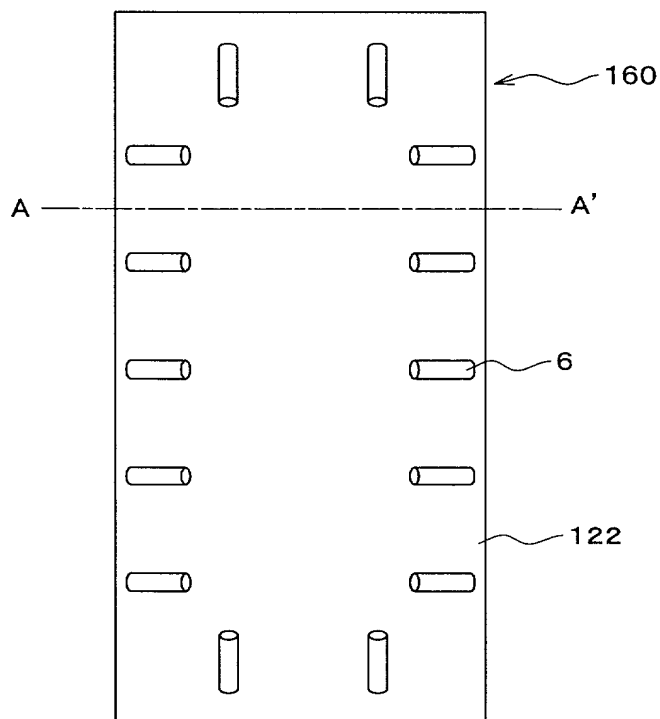


Fig. 32B

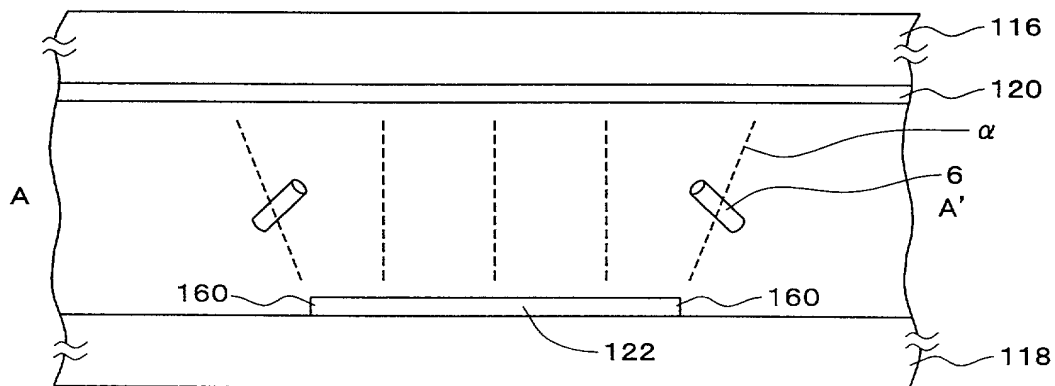


Fig. 33A

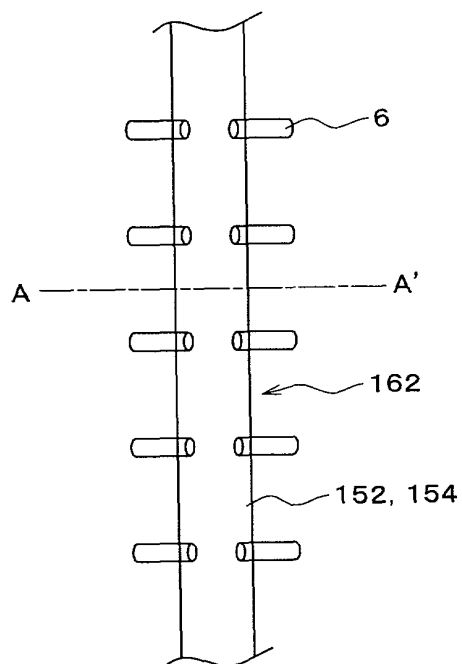


Fig. 33B

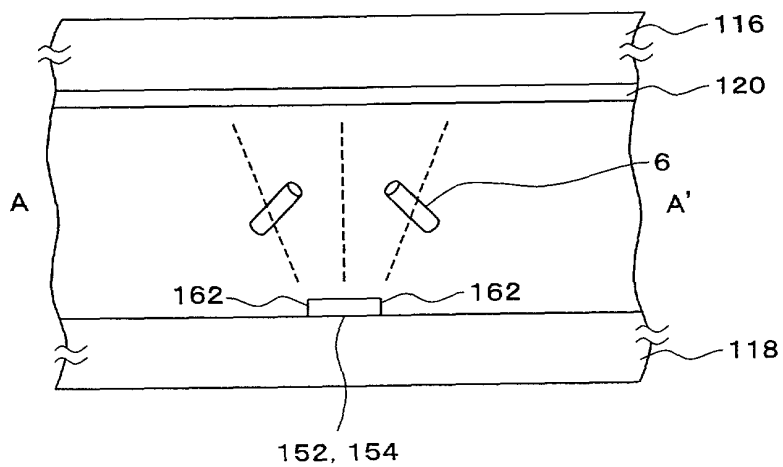


Fig. 34

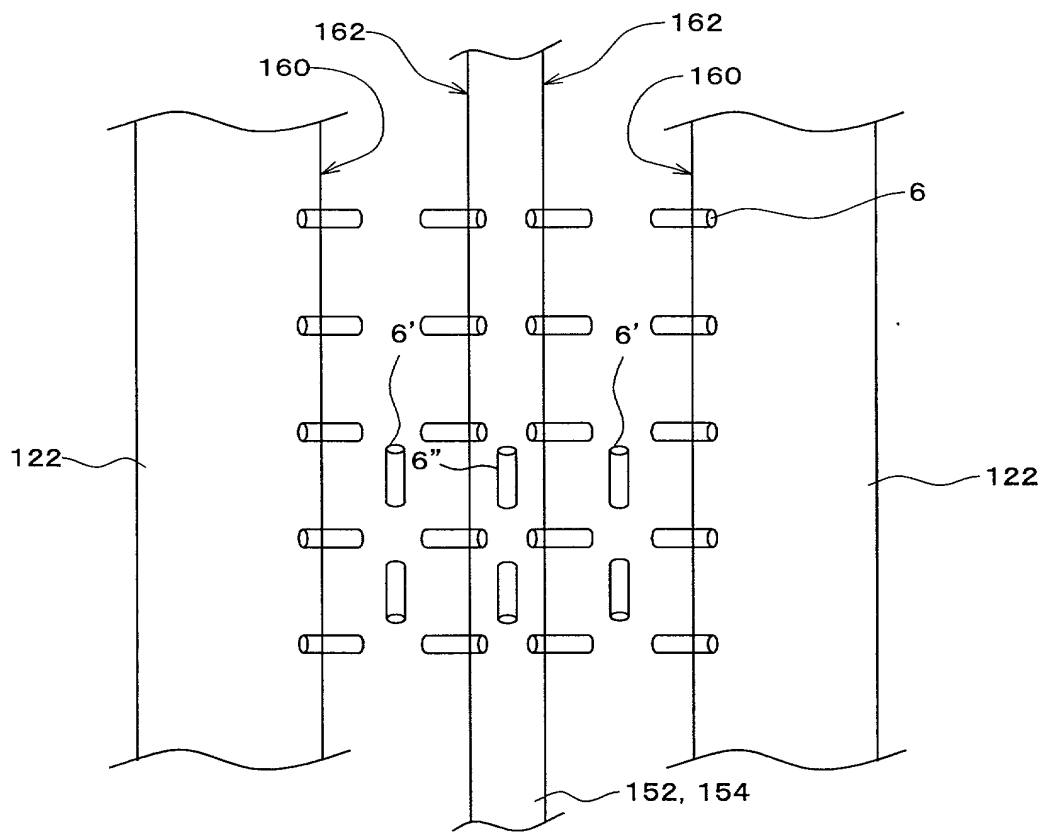


Fig. 35

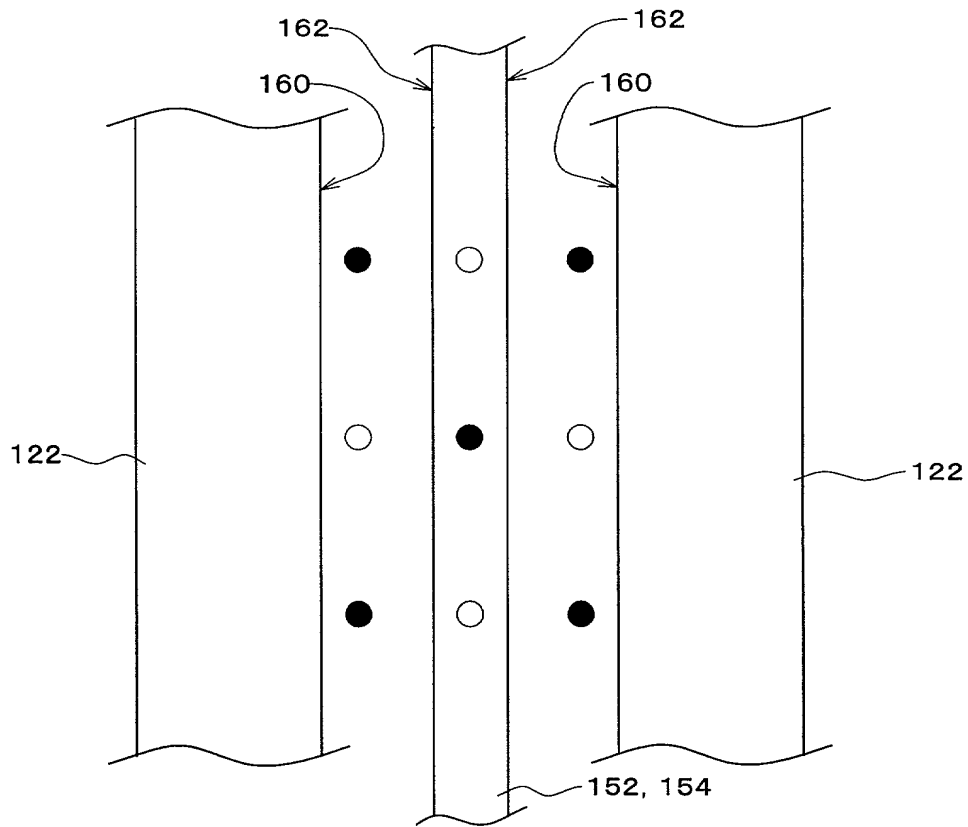




Fig. 36

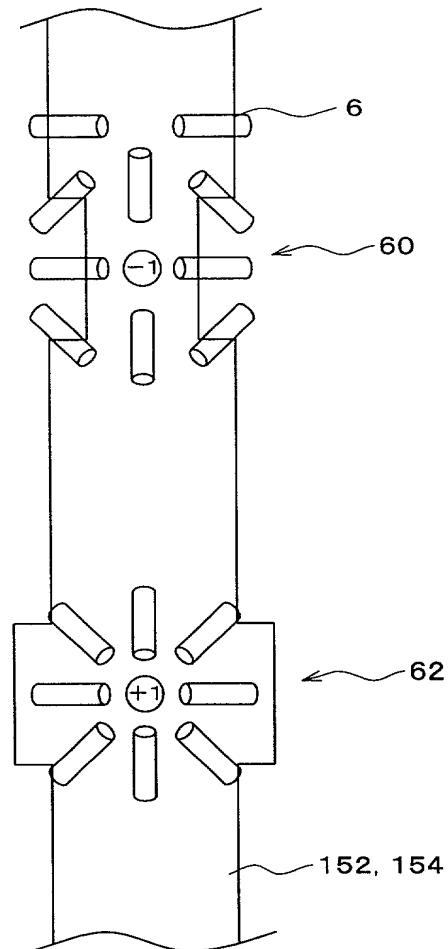


Fig. 37A

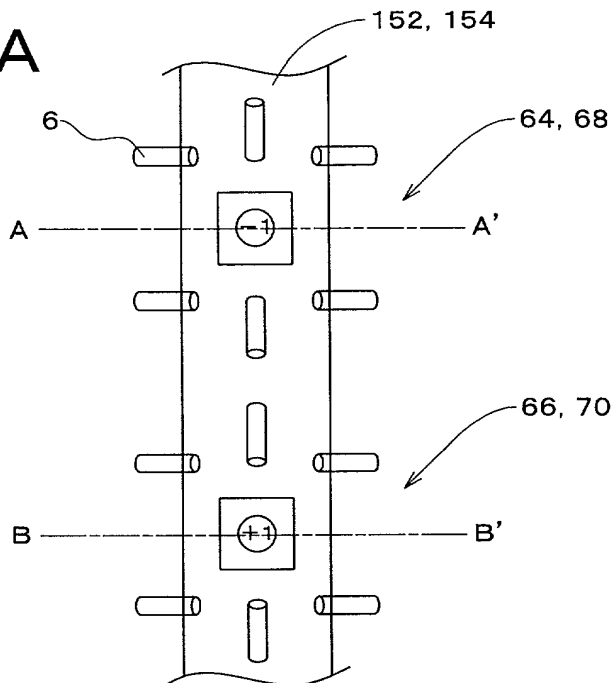


Fig. 37B

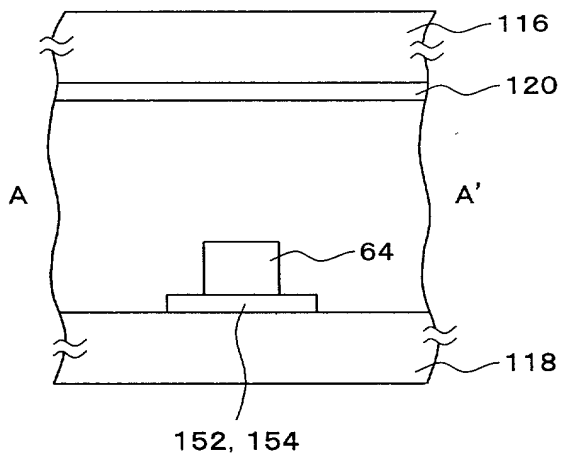


Fig. 37D

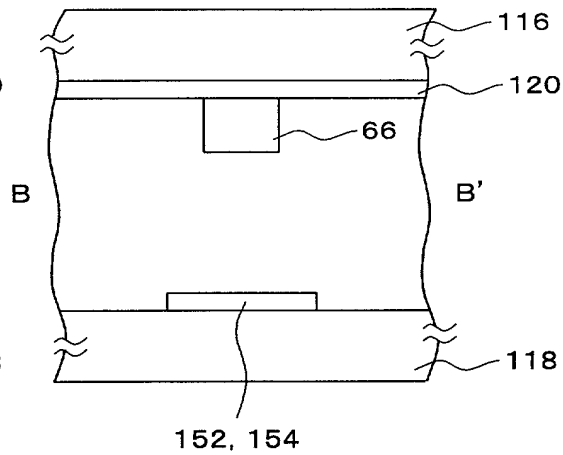


Fig. 37C

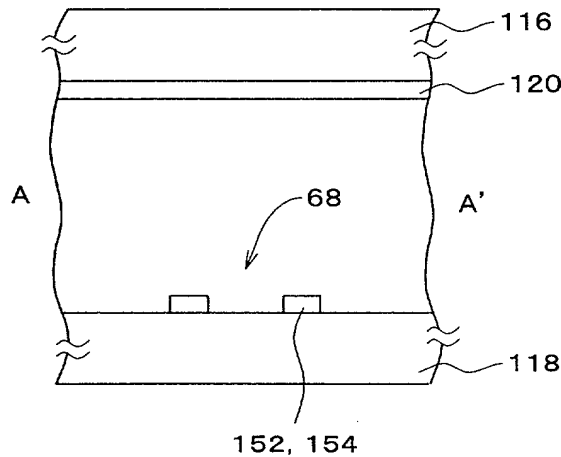


Fig. 37E

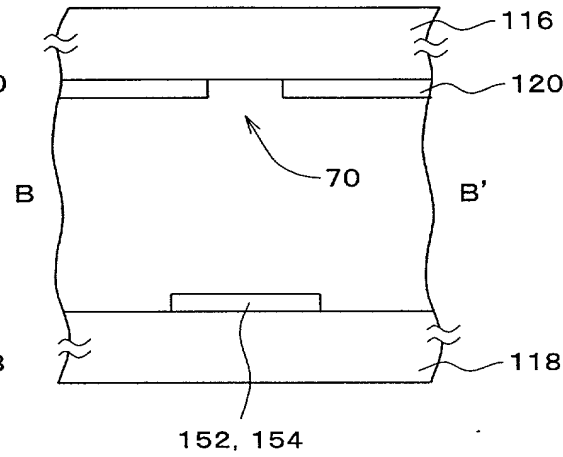


Fig. 38

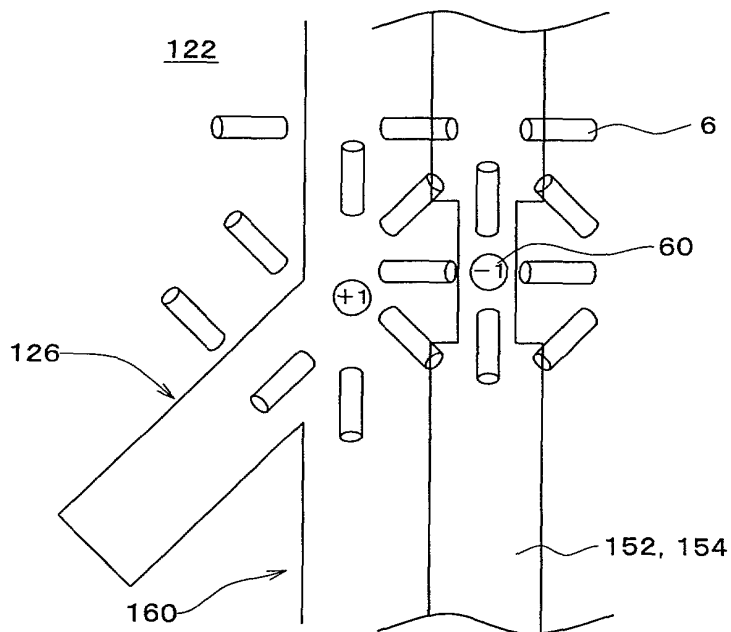


Fig. 39

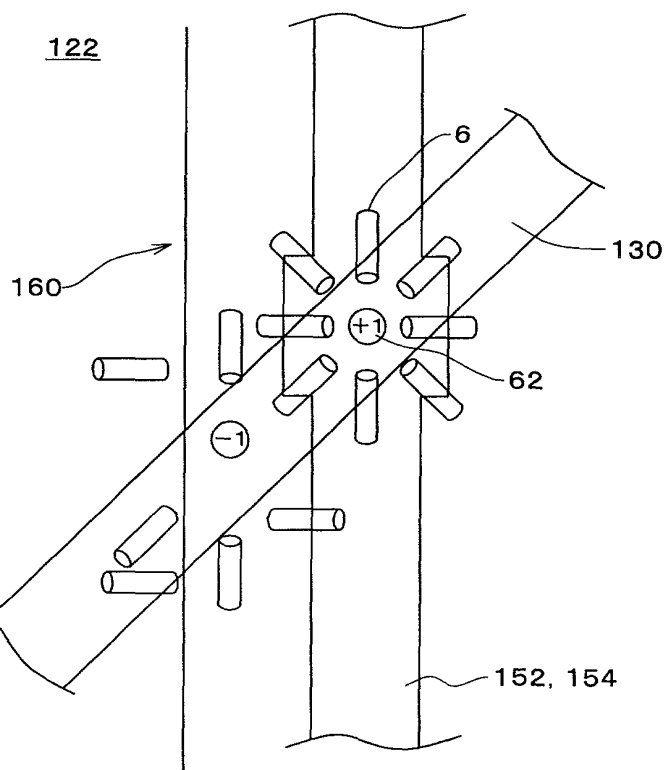


Fig. 40

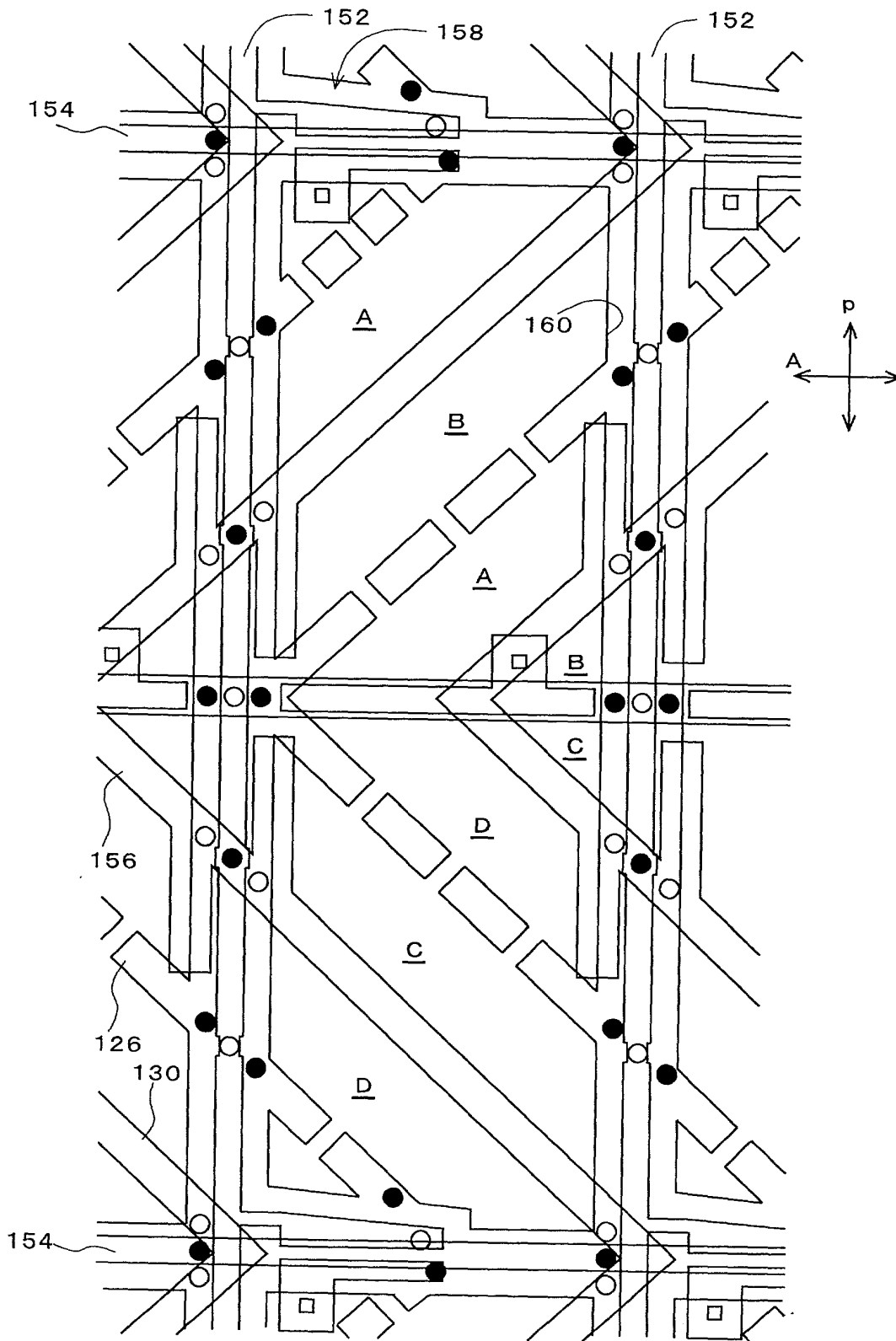


Fig. 41

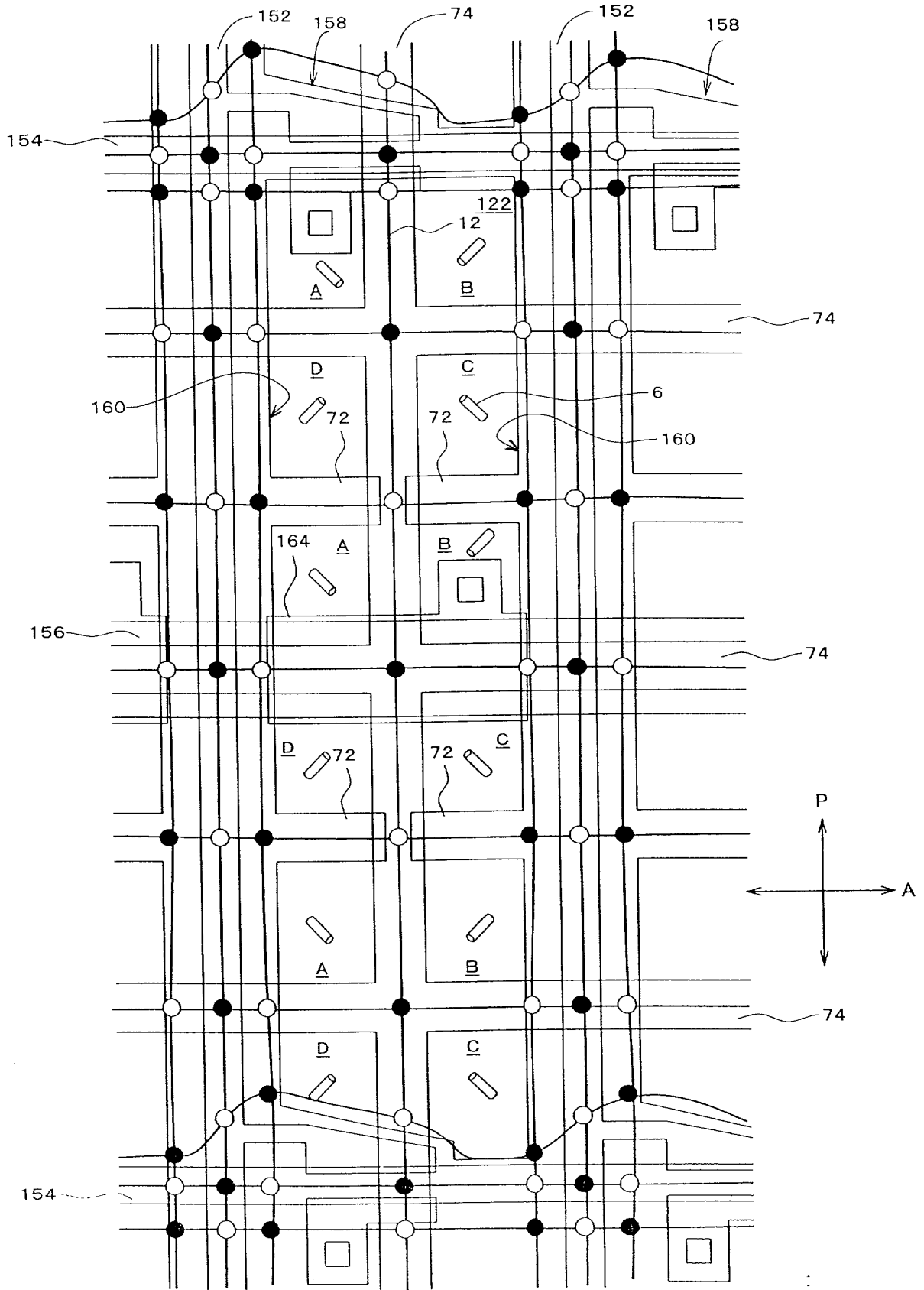


Fig. 42A(PRIOR ART)

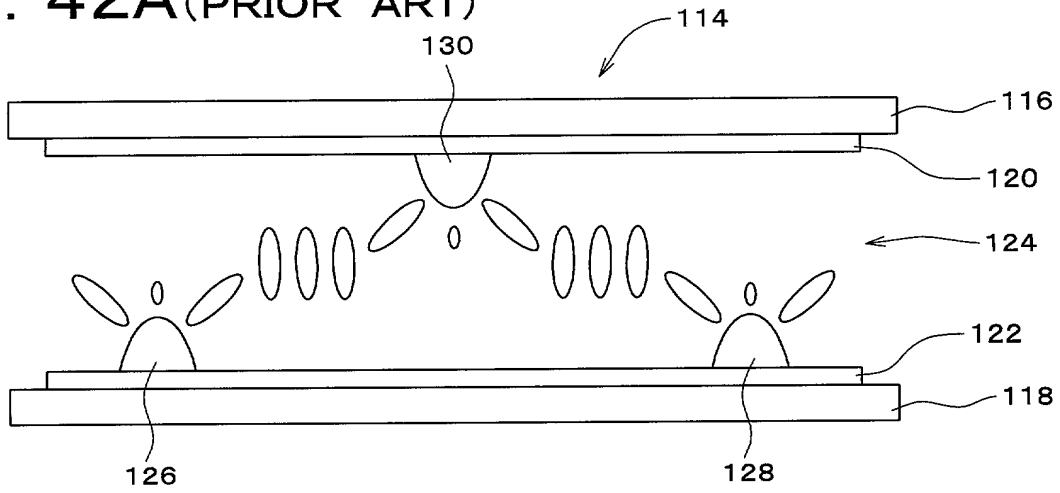


Fig. 42B(PRIOR ART)

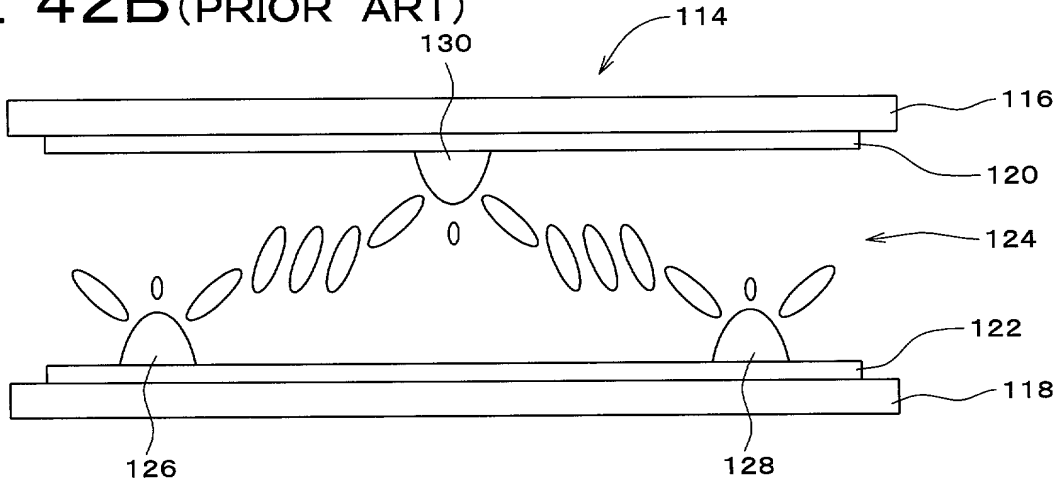


Fig. 42C(PRIOR ART)

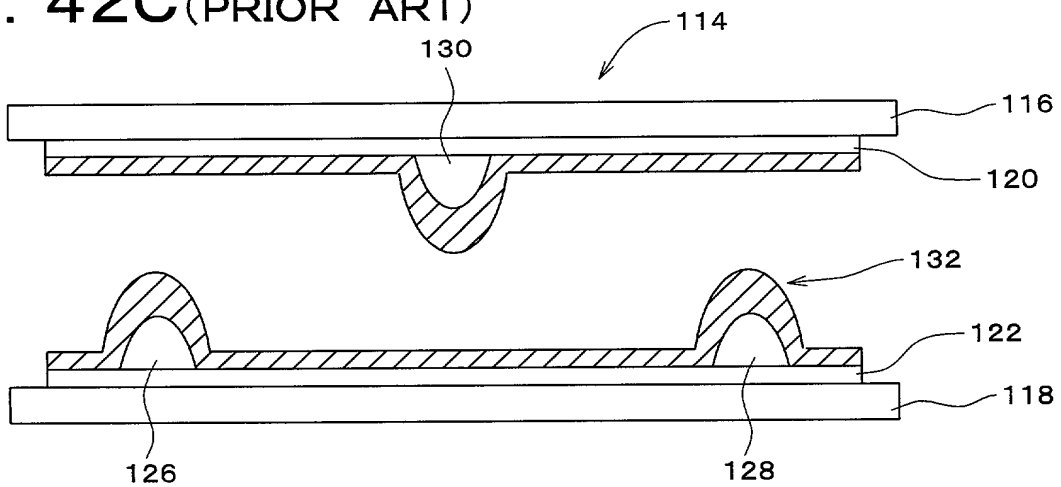


Fig. 43A(PRIOR ART)

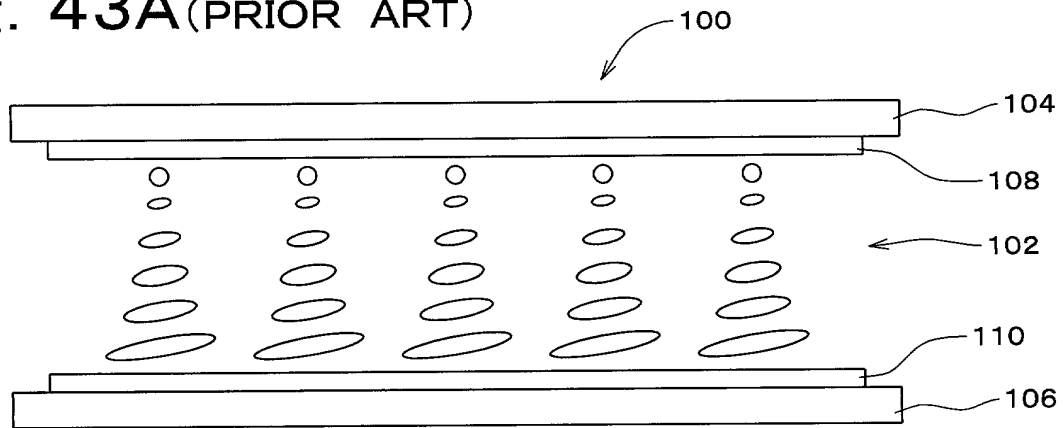


Fig. 43B(PRIOR ART)

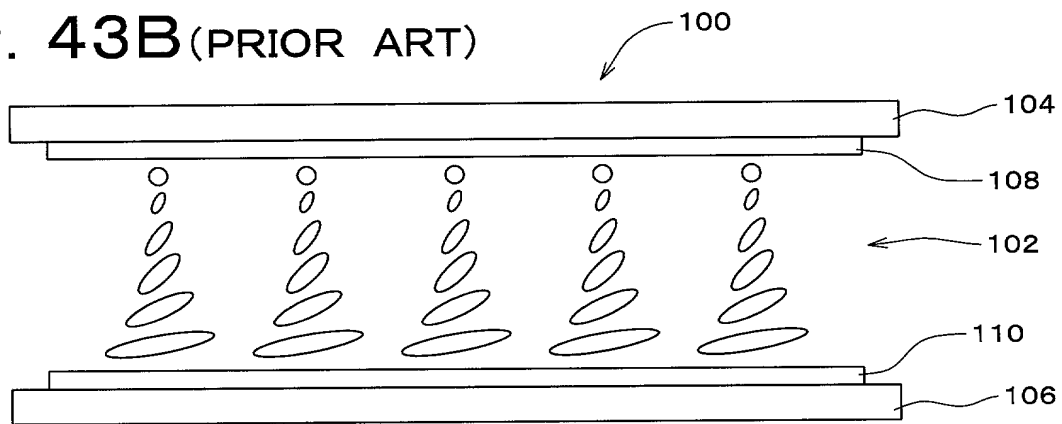


Fig. 43C(PRIOR ART)

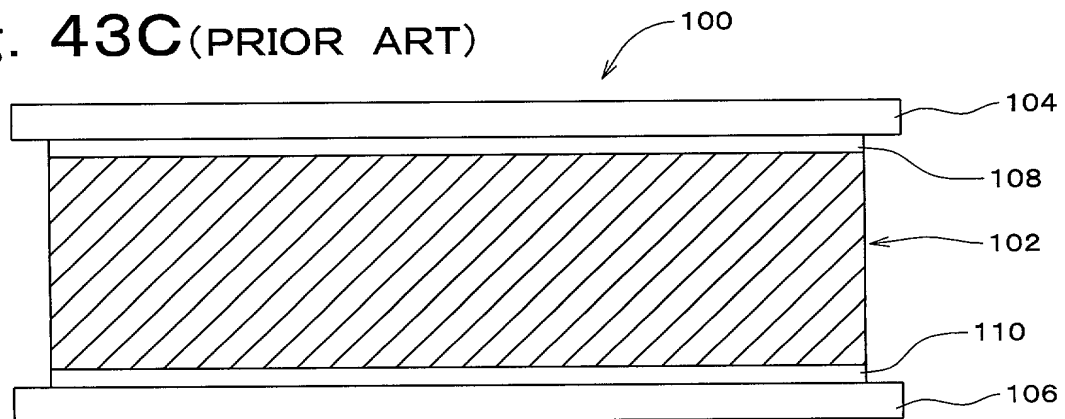


Fig. 44 (PRIOR ART)

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